



FEATURES

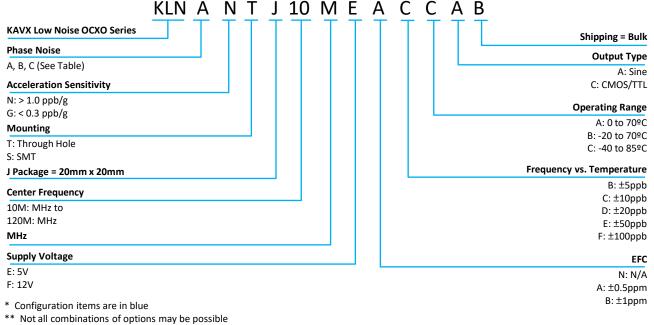
- Thru Hole or Surface Mountable
- Superior Frequency Stability
- Ultra Low Phase Noise
- Low Acceleration Sensitivity
- Tolerant to Micro Vibrations
- Low Age Rates
- 20 x 20mm Package

KYOCERA AVX's ultra low phase noise OCXO product offering is a result of 90+ years of leading products within the Frequency Control Industry. Modern layout topologies enable KYOCERA AVX to engineer and manufacture robust designs for all applications.





HOW TO ORDER



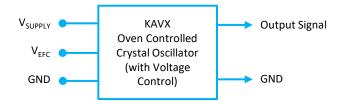
APPLICATIONS

Network Infrastructure

*** Other options may be available

- Phase Locked Microwave Applications
- Low Noise Test and Measurement Systems
- GPS Precision Timing Devices
- Military High-Performance Devices
- Medical Devices
- Aerospace
- Industrial

BLOCK DIAGRAM



Note: If EFC Option "N" is used, connect V_{EFC} to GND



The Important Information/Disclaimer is incorporated in the catalog where these specifications came from or available online at www.kyocera-avx.com/disclaimer/ by reference and should be reviewed in full before placing any order



PERFORMANCE SPECIFICATIONS

Parameter	Conditions		Values		Unit
		MIN	ТҮР	MAX	
Frequency Range		10		120	MHz
Initial Tolerance	10 MHz @ +25ºC (Nominal)			±100	ppb
	100 MHz @ +25ºC (Nominal)			±250	ppb
Warm Up Time	To initial tolerance			5	Min
Frequency Stability					
vs. Temperature	Options B - (Max-Min)/2		±5		ppb
	Options C - (Max-Min)/2		±10		ppb
	Options D - (Max-Min)/2		±20		ppb
	Options E - (Max-Min)/2		±50		ppb
	Options F - (Max-Min)/2		±100		ppb
vs. Load	\pm 5% Δ in Load		±2		ppb
vs. Supply Voltage	± 5% Δ in supply		±2		ppb
ADEV (Short Term Stability)	T = 1 second		5E-12		
Aging	After 30 Days Operation				
Per Day	10 MHz			±1.0	ppb
1 st Year	10 MHz			±100	ppb
Per Day	100 MHz			±5.0	ppb
1 st Year	100 MHz			±500	ppb
Supply Voltage (Vdd)	Option E	4.75	5	5.25	Vdc
	Option F	11.4	12	12.6	Vdc
Power Dissipation					
Start Up	@ +25ºC (Nominal)			5.2	W
Steady State	@ +25°C (Nominal)		1.5		W
Electronic Frequency Control					
Voltage Range	Vdd = 5 Vdc	0	2.5	5	Vdc
	Vdd = 12 Vdc	0	5	10	Vdc
Frequency Range	Option N	0			ppm
	Option A	±0.5			ppm
	Option B	±1.0			ppm
Slope			positive		
Input Impedance			100		kΩ
Linearity			10		%

Note: Values typical of 10MHz units unless defined within





PERFORMANCE SPECIFICATIONS

Parameter	Conditions		Values		Unit
Output Characteristics (CMOS/T	ГL)	MIN	TYP	MAX	
High Output Level	Logic "1"	90% Vdd			Vdc
Low Output Level	Logic "0"			10% Vdd	Vdc
Rise/Fall Time				5	nSec
Duty Cycle		45	50	55	%
Load			15		pF
Output Characteristics (Sinusoid)		MIN	TYP	MAX	
Output Level		10		16	dBm
Spurious				-80	dBc
Harmonics				-30	dBc
Load		47.5	50	52.5	Ω

Parameter	Conditions		Values		Unit
Phase Noise		A ^{1,2}	B ¹	С	
Phase Noise (10 MHz)	Tested at +25°C (Nominal)				
	10Hz	-130	-125	-120	dBc/Hz
	100Hz	-155	-152	-140	dBc/Hz
	1kHz	-166	-160	-145	dBc/Hz
	10kHz	-173	-170	-155	dBc/Hz
	100kHz	-175	-170	-155	dBc/Hz
Phase Noise (100 MHz)	Tested at +25°C (Nominal)				
	10Hz	-103	-100	-95	dBc/Hz
	100Hz	-133	-130	-125	dBc/Hz
	1kHz	-157	-155	-145	dBc/Hz
	10kHz	-172	-168	-160	dBc/Hz
	100kHz	-175	-172	-170	dBc/Hz

^{1.} Specific Phase Noise performance is subject to Export Control restrictions from the U.S.

Note: Values typical of 10MHz units



^{2.} Please contact Kyocera AVX to analyze Phase Noise requirements that must be lower

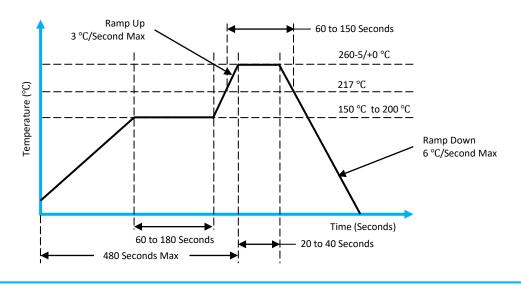


ENVIRONMENTAL COMPLIANCE

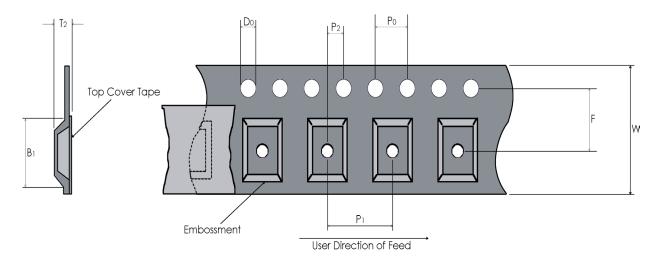
Parameter	Conditions		Values		Unit
		MIN	TYP	MAX	
Operating Temperature	Option A	0		+70	°C
	Option B	-20		+70	°C
	Option C	-40		+85	°C
Storage Temperature		-55		+100	°C
Seal	MIL-STD-202 Method 112 Test Condition D				
Shock	MIL-STD-202G Method 213 Test Condition C				
Random Vibration	MIL-STD-810G Method 514 Test Procedure I				
Sinusoidal Vibration	MIL-STD-202G Method 204 Test Condition A				
MTTF	Calculated using MIL-HDBK-217		153,300		Hours
Acceleration Sensitivity	10MHz output Vibration profile: 0.001G ² /Hz 10Hz to 2kHz		1.0		ppb/g
	"G" Option			0.3	ppb/g



ACCEPTABLE REFLOW PROFILE



TAPE AND REEL



Tape Dimensions (mm) Reel Dimensions (mm)							ons (mm)		
W	F	Do	Ро	P1	P2	B1	T2	Outside Dia.	Parts / Reel
44	20.5	1.5	4.0	36	2.0	20.6	19.7	330	250

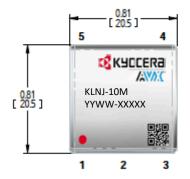
Notes:

- 1. Profile Classification per IPC/JEDEC J-STD-020C Pb-Free Small Body Assembly
- 2. Only the SMT version can be selected as a Tape & Reel shipping method
- 3. If Tape & Reel is required a MOQ of 200-piece increments are required.

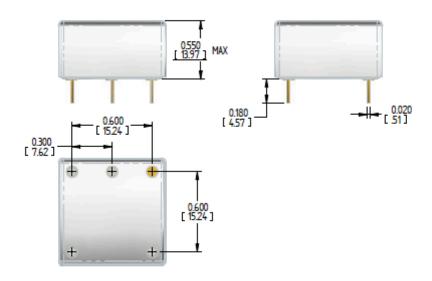




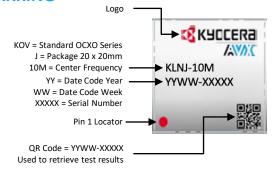
MECHANICAL SPECIFICATIONS – THROUGH HOLE







MARKING



Tolerances (mm) $.X = \pm 0.5$, $.XX = \pm 0.2$ unless otherwise specified

PIN	FUNCTION			
1	Supply Voltage			
2	RF Output			
3	Ground			
4	EFC / N.C.			
5	N.C.			







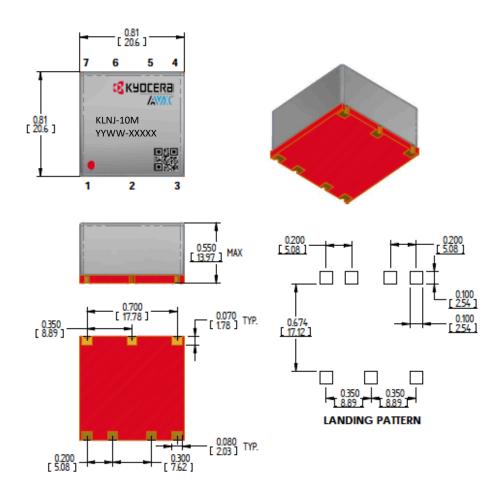
Notes

Non-RoHS available upon request

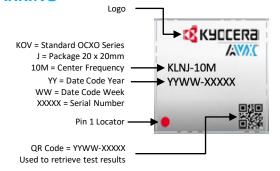




MECHANICAL SPECIFICATIONS – SURFACE MOUNT



MARKING



Tolerances (mm) $.X = \pm 0.5$, $.XX = \pm 0.2$ unless otherwise specified

PIN	FUNCTION				
1	Supply Voltage				
2	RF Output				
3	Ground				
4	EFC / N.C.				
5, 6, 7	N.C.				







Notes

Non-RoHS available upon request

